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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/724,263	12/01/2003	Toshiyuki Kasai	117782	1631
25944	7590	12/12/2006	EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320				CHOW, DOON Y
		ART UNIT		PAPER NUMBER
		2629		

DATE MAILED: 12/12/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/724,263	KASAI, TOSHIYUKI	
Examiner	Art Unit		
Dennis-Doon Chow	2629		

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

## Disposition of Claims

4)  Claim(s) 1-23 is/are pending in the application.  
4a) Of the above claim(s) 4,10-17 and 22 is/are withdrawn from consideration.

5)  Claim(s) \_\_\_\_\_ is/are allowed.

6)  Claim(s) 1-3,5,6,18-21 and 23 is/are rejected.

7)  Claim(s) 7-9 is/are objected to.

8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)  The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All b)  Some \* c)  None of:  
1.  Certified copies of the priority documents have been received.  
2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_

4)  Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_ .

5)  Notice of Informal Patent Application

6)  Other: \_\_\_\_\_

**DETAILED ACTION**

1. Applicant's election with traverse of Species I, Figure 2, claims 1-3, 5-9, 18-21, and 23 in the reply filed on September 28, 2006 is acknowledged. The traversal is on the ground(s) that the subject matter of all species is sufficiently related that a thorough search for the subject matter of any one species would encompass a search for the subject matter of the remaining species. Thus, the search and examination of the entire application could be made without serious burden. This is not found persuasive because it is not true that a search for the subject matter of any one species would encompass a search for the subject matter of the remaining species. A different search is clearly required for a different Species.

The requirement is still deemed proper and is therefore made FINAL.

***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1-2, 3, 5, 18-21 and 23 are rejected under 35 U.S.C. 102(e) as being anticipated by Sekiya et al. (6583775).

Regarding to claims 1-2, and 18-20, Sekiya discloses an electro-optical device and a method of driving the electro-optical device, comprising: a plurality of scanning lines (X1...Xn, Figs. 2 and 4); a plurality of data lines (Y, Figs. 2 and 4); a plurality of pixels (PXL, Fig. 2) located at intersections of the scanning lines and the data lines; a scanning-line driving circuit (21, Fig. 2) that outputs the scanning signal to the scanning lines to select the scanning line corresponding to a pixel to which data is written; and a data-line driving circuit (22, Fig. 2) that cooperates with the scanning-line driving circuit and that outputs data to the data line corresponding to the pixel to which data is written, each of the pixels including: an electro-optical element (OLED, Figs. 1 and 5) that emits light with a brightness corresponding to a driving current; a capacitor (Cs, Figs. 1 and 5) that stores an electric charge corresponding to the data supplied via the data line to write the data; a drive transistor (TFT2, Fig. 1 and 5) that sets the driving current according to the electric charge stored in the capacitor and supplying the driving current to the electro-optical element; and a control transistor (TFT3, Fig. 1 and 5) that repeatedly interrupts a current path for the driving current for a period after the scanning line corresponding to the pixel to which data is written is selected until a next time this scanning line is selected (cols. 11, lines 1-39; col. 13, line 62 to col. 14, lines 18).

Regarding to claims 3 and 21, Sekiya further discloses a programming transistor (TFT1, Figs. 1 and 5) for performing data writing to the capacitor based on a gate voltage generated by causing the data current to flow in a channel of the programming transistor (col. 10, lines 60-68).

Regarding to claims 5 and 23, Sekiya further discloses the control transistor being turned on or off under control of a pulse signal output from the scanning-line driving circuit, and the scanning-line driving circuit converting the pulse signal supplied to the pixel to which data is written to a signal with pulse form which alternates between a high level and a low level in synchronization with the scanning signal supplied to the pixel to which data is written (col. 10, line 42 to col. 11, lines 39; col.13, line 62 to col. 14, lines 18).

4. Applicant cannot rely upon the foreign priority papers to overcome the following rejection because a translation of said papers has not been made of record in accordance with 37 CFR 1.55. See MPEP § 201.15.

5. Claim 6 is rejected under 35 U.S.C. 102(e) as being anticipated by Hiroshi (JP 2003-216100).

Hiroshi discloses an electro-optical device, comprising: a plurality of scanning lines (17a1, 17a2, Fig. 424); a plurality of data lines (1, Fig. 424); a plurality of pixels (16, Fig. 424) located at intersections of the scanning lines and the data a scanning-line driving circuit that outputs a first scanning signal to the scanning lines so as to select the scanning line corresponding to a pixel to which data is written and that outputs a second scanning signal synchronous with the first scanning signal and a pulse signal synchronous with the first scanning signal; and a data-line driving circuit that cooperates

with the scanning-line driving circuit for outputting a data current to the data line corresponding to the pixel to which data is written, each of the pixels including: a first switching transistor (11c, Fig. 424) having one of a source terminal and a drain terminal coupled with the data line so as to be controlled by the first scanning signal; a second switching transistor (11d, Fig. 424) having one of a source terminal and a drain terminal coupled with the other terminal of the first switching transistor so as to be controlled by the second scanning signal; a capacitor (16, Fig. 424) coupled with the other terminal of the second switching transistor; a programming transistor (1a, Fig. 424) having a drain commonly coupled with the other terminal of the first switching transistor and the one terminal of the second switching transistor, and a gate commonly coupled with the other terminal of the second switching transistor and the capacitor, so that an electric charge corresponding to the data current is stored in the capacitor connected with the gate of this programming transistor; a drive transistor (11b, Fig. 424) paired with the programming transistor to form a current mirror circuit that sets a driving current according to the electric charge stored in the capacitor, which is connected with a gate thereof; an electro-optical element (15, Fig. 424) that emits light with a brightness corresponding to the driving current; and a control transistor (11e, Fig. 424) provided in a current path for the driving current that interrupts the current path for the driving current under conduction control of the pulse signal (1tb, Fig. 424).

***Allowable Subject Matter***

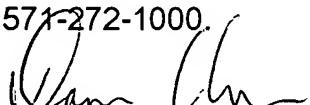
6. Claims 7-9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

### ***Conclusion***

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dennis-Doon Chow whose telephone number is 571-272-7767. The examiner can normally be reached on 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on 571-272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



Dennis-Doon Chow  
Primary Examiner  
Art Unit 2629

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D. Chow  
December 7, 2006